

General Rules and Guidelines for Design Projects 2018

- There is no collaboration in Design Projects. Each and every student will take a single design project topic and follow up with it throughout the semester. Two students may be assigned to a single/the same topic. However, each of the individual should carry out the Project on his/her own.
- Any project that contains programming to any extent will be applied to source code plagiarism tools. Fundamental functions, building blocks, and/or core parts/designs of the software to be developed will be checked with source code plagiarism tools. Any project yielding more than 5% overlap with any other source code present in the tool repository will be graded directly with letter grade "F". Furthermore, supervisor will submit a formal complaint to Committee on Discipline. A discipline file including the letter defining the type of violation will be formed and documented regarding academic integrity and dishonesty rules and guidelines. The discipline file then will be stored and affiliated with the student's academic records.
- Students will be assigned to projects rather than advisors. Which project will be supervised by which advisor will be announced after student-project mappings are finalized by the Department.
- Advisor/department head can override student choices if it is necessary.

Design Project/Graduation Project Topics

Energy harvesting telemetering system
Rubick's cube solving robot
Remote control sniffer and protocol analyzer
Gimbal design
ECG sensor
Image transfer on oscilloscope display
Turkcell projects* x2
Digital controlled FM transmitter
Morse Code-based wireless instant messaging device
Lie detector prototype
Smart prosthesis arm
Home appliances power profile identification
G-code optimizer
High precision small weight electronic scale
Acoustic direction finder
Universal TV remote control
CPU/ALU design
Autonomous 2-D layout/border finding robot
Video display scrambler/descrambler system
Relaying TV signals with wireless relay
Panaromic image reconstruction
Power/cable network diagnosis (cable fault finder, cable network probe, etc.)

* These projects will be governed, validated, and evaluated by Turkcell! Therefore, special requirements should be met by the students who wish to be assigned those projects.